



Docket No.: 251272US-2S RE



COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

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RE: Application Serial No.: 10/817,623
Applicants: Toru TAKEDA, et al.
Filing Date: April 5, 2004
For: MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE
Group Art Unit: 2891
Examiner: FULK, STEVEN J.

SIR:

Attached hereto for filing are the following papers:

SUPPLEMENTAL REISSUE APPLICATION DECLARATION BY THE INVENTOR

Credit card payment is being made online (if electronically filed), or is attached hereto (if paper filed), in the amount of **\$0.00** to cover any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. **15-0030**. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time.

Respectfully submitted,

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